

## INFORMATION DISCLOSURE CITATION

Atty. Docket No. 04173.0440	Application No.
Applicant Mie MATSUO	
Filing Date January 20, 2004	Group:

U.S. PATENT DOCUMENTS						
Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate
BS	5,424,245	06/13/1995	Gurtler et al.			

FOREIGN PATENT DOCUMENTS						
Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No	
BS	10-223833	08/21/1998	Japan		Abstract	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
BS	Nobuo HAYASAKA, et al.; Multichip Semiconductor Device, Chip Therefor and Method of Formation Thereof; U.S. appln. no. 09/377,486, filed Aug. 20, 1999.

Examiner Ben Sandvik	Date Considered 4/4/05
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	
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